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Application Number	10/601,351
Filing Date	June 23, 2003
First Named Inventor	Crafts, Harold S.
Art Unit	2812
Examiner Name	
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Examiner Initials*	Cite No.1	Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
OH	_ AA	US-4,742,383	05/03/1988	Fitzgerald	
DH	AB	US-5,410,173	04/25/1995	Kikushima et. al	
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Application Number

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First Named Inventor

Crafts, Harold S.

Art Unit
Examiner Name

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000939-052170US

		NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item	т
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Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. Applicant is to place a check mark here if English language Translation is attached.

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1	BI	US-5,225,991	07/08/1993	Dougherty		
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Substitute for form 1449/PTO Complete if Known **Application Number** INFORMATION DISCLOSURE Filing Date STATEMENT BY APPLICANT First Named Inventor Crafts, Harold S. Art Unit (use as many sheets as necessary) **Examiner Name** Page Attorney Docket Number 000939-052170US

·		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
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DAY	CA	IBM Technical Dislosure Bulletin; Vol. 25, No. 4; September 1982; Structured Macro; E.H. STOOPS	
	СВ	An Efficient Algorithm for some Mitirow Layout Problems; J. FELDMAN et al.; IEEE Transactions on Computer-Aided Design of Integrated Circuits & Systems; Vol. 12, No. 8, August 1993; pp. 1178-1185	
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	AG	GaAs MESFET and SiCMOS Cointegration & Circuit Techniques; H. SHICHIJO et al.; IEEE, GaAs IC Symposium, September 1988; pp. 239-242	
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